

Fig. 1. Percentage difference in the two expressions for the mean-squared error.

Substituting for σ^2 , the error in the output of the filter due to this source of product quantization error becomes

$$E[|V^i|^2] = \frac{E_0}{3} [E_0 + 2^{-(m+n)}] \sum_{k=0}^i |h_k|^2. \quad (10)$$

Thus, the difference in the model for product quantization affects the output error as seen in (10). This result is general enough to consider different quantization levels for the input, coefficients and products of a digital filter. For example, if the input is rounded to m binary bits, the coefficients to n bits, and the products (hence, the output) to 1 bits, the mean square of the error in the output due to the quantization of the products is given by (10). The usual model gives the same

value for any quantization level that is used for the input and coefficients.

Fig. 1 shows a plot of the percentage difference E between the two expressions for the mean squared error due to product quantization for various values of $m+n$.

$$E = \frac{(E_0/J)(E_0 + 2^{-(m+n)}) - (\#_{73})}{(E_0/3)} \times 100 = \frac{2^{-(m+n)}}{E_0} \cdot (11)$$

For a fixed quantization level, say rounding to 1 binary bits, the percentage difference becomes smaller as $m+n$ increases. This is expected since as $m+n$ increases, the product to be quantized approaches a continuous variable; hence, the difference between the two expressions becomes smaller. For similar reasons, when $m+n$

is fixed the percentage difference increases as 1 increases. It should also be noted that when $m=n=1$, the percentage difference decreases as the quantization level increases.

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References

- [1] B. Gold and C. M. Rader, *Digital Processing of Signals*. New York: McGraw-Hill, 1969, ch. 4.
- [2] N. Halyo, "Quantization error analysis in digital filters," M.S. thesis, Univ. Virginia, Charlottesville, June 1970.
- [3] W. R. Bennett, "Spectra of quantized signals," *Bell. Syst. Tech. J.*, vol. 27, July 1948, pp. 446-472.

An Active Feedback Block and its Applications

Abstract

The active feedback block considered in this correspondence consists of three identical single-pole amplifiers and a resistive network, and has a positive slope in the gain frequency characteristics over a certain frequency range starting from dc. When the device is cascaded to another single-pole amplifier, the bandwidth and the gain-bandwidth product of the overall system are found to be increased compared to those of a cascaded two-stage amplifier.

High-frequency compensation of amplifiers is a classical subject and standard methods, using inductance and passive feedback techniques, are described in detail in standard textbooks on electronic circuits. In microcircuits, one cannot use an inductance; on the other hand, passive feedback techniques have their limitations with regard to the total gain-bandwidth product (GBW). In this communication, we propose a different approach to compensation, using the so-called "active feedback block" (AFB), which is particularly suitable for microcircuit construction and application. The schematic diagram of the AFB is shown in Fig. 1(A), where amplifiers 1, 2, and 3 are identical, each having a gain

$$A_i(s) = -A_0 \omega_0 / (s + \omega_{01}), \quad A_0 > 0, \quad (1)$$

and P_0 represents the transfer function of a

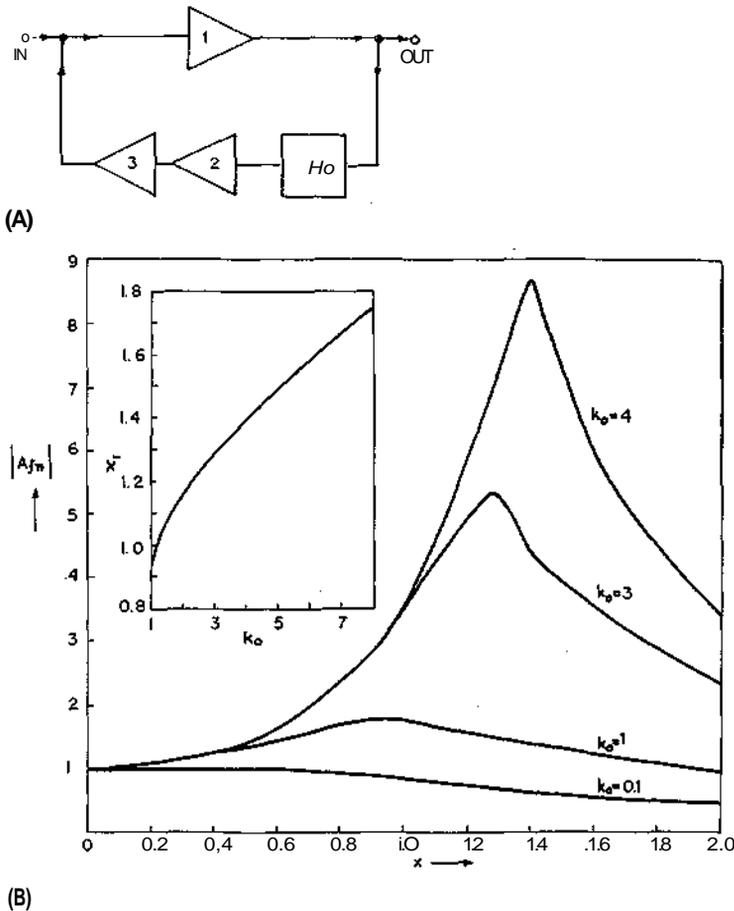


Fig. 1. (A) Schematic diagram of the active feedback block. (B) Variation of x_1 with k_o and the frequency response curves of the AFB.

resistive network in the feedback path. The overall gain of the AFB is given by $A_f(s) = -A_{n0}(s + \omega_0) / [(\omega_0 + \omega_0) + k\omega_0 s]$ (2)

where $k\omega_0 = A_{n0} \omega_0^3$. Normalizing the gain with respect to its dc value $A_f(0) = A_{n0} / (\omega_0 + k\omega_0)$, and the complex frequency s with respect to ω_0 , we get

$$\frac{A_f(s/\omega_0)}{A_f(0)} = \frac{(1 + k\omega_0)}{(s/\omega_0 + 1)(3 + k\omega_0 s/\omega_0 + 1)} \quad (3)$$

where $s/\omega_0 = x$. Equation (3) shows that,

$$\frac{d|A_{fn}(jx)|}{dx} = \frac{(1 + k\omega_0)x[(2k\omega_0^2 + 10k\omega_0 - 1) - 3(2k\omega_0 + 1)x^2 - 3x^4 - x^6]}{[(1 + k\omega_0 - 3a_1^2)^2 + z^2(3 - a_1^2)]^{3/2}} \quad (4)$$

depending on $k\omega_0$, the AFB may be stable or unstable. By applying the Routh-Hurwitz criterion, the condition of stability can be easily shown to be $k\omega_0 < 8$. For

$k\omega_0 = 8$, (3) has $j\omega$ axis poles at $s_n = \pm j\sqrt{3}$ i.e., the system will oscillate at a frequency $\sqrt{3}\omega_0$.

Applying Bode plot considerations to (3), it appears possible to get a positive slope in the gain frequency sketch in a certain frequency range. In order to investigate this, we let $s = j\omega$ (sinusoidal excitation) and $x = \omega/\omega_0$ in (3) and differentiate the magnitude of the resulting expression with respect to x . The result is, after some algebraic manipulations,

It is observed from (4) that at sufficiently high frequencies, the slope will always be negative and that the condition for positive slope is

$$2k_0^2 + 10k_0 - 1 > 0, \text{ i.e., } k_0 > 0.0975. \quad (5)$$

If (5) is satisfied, then the slope will be nonnegative for a certain range of frequencies $0 \leq x \leq x_1$ and will be negative for $x > x_1$. We can find x_1 by equating (4) to zero; the result is

$$x_1 = [(a + b)^{1/3} - (a - b)^{1/3}] / 2$$

where

$$a = k_0(k_0^2 + 24k_0 + 64)^{1/2}$$

and

$$b = k_0(k_0 + 8). \quad (6)$$

Fig. 1(B) shows the plot of x_1 versus k_o and also the frequency response for several values of k_o .

The rising gain frequency characteristics of the AFB in the range $0 \leq x \leq x_1$ can be used to compensate for the high-frequency falloff of an amplifier. Consider, for illustration, the system shown in Fig. 2(A), which is a cascade of the AFB and an amplifier having the gain given by (1). The overall gain of this system is $A_T(s) = A(s)A_f(s)$; normalizing this with respect to the dc value $A_T(0) = A(0)A_f(0)$, we get the following magnitude squared response for sinusoidal excitation.

$$|A_T(jx)|^2 = \frac{(1 + k\omega_0)^2(1 + x^2)}{(1 + k\omega_0)^2 + 3x^2(1 + 2k\omega_0 + 3x^2 + x^4)} \quad (7)$$

From (7), the condition for maximally flat response can be easily shown to be $k\omega_0 = 0.242$; the frequency response of (7) under this condition is shown in Fig. 2(B), where from the 3-dB bandwidth x_{3dB} is found to be 0.938. The gain-bandwidth product of compensated amplifier is given by

$$GBW \triangleq A_T(0)x_{3dB}\omega_0 = [x_{3dB}/(1 + k\omega_0)]A_0\omega_0 \quad (8)$$

For the maximally flat case, therefore,

$$GBW_{MF} = (0.754A_0)(4A_0\omega_0) \quad (9)$$

We thus see that the AFB acts as a GBW booster to the single-stage amplifier A_1 ; however, it reduces the bandwidth slightly. If $k\omega_0 = 0$, i.e., there is no feedback in the AFB, then Fig. 2(A) becomes a cascade of two identical stages, for which x_{3dB} is known to be 0.644; thus from (8),

$$GBW_{1-stage cascade} = (0.644A_0)(A_0)(A_0\omega_0) \quad (10)$$

Thus, compared to the two-stage cascade, the compensated amplifier has 1.46 times higher bandwidth and 1.17 times higher GBW.

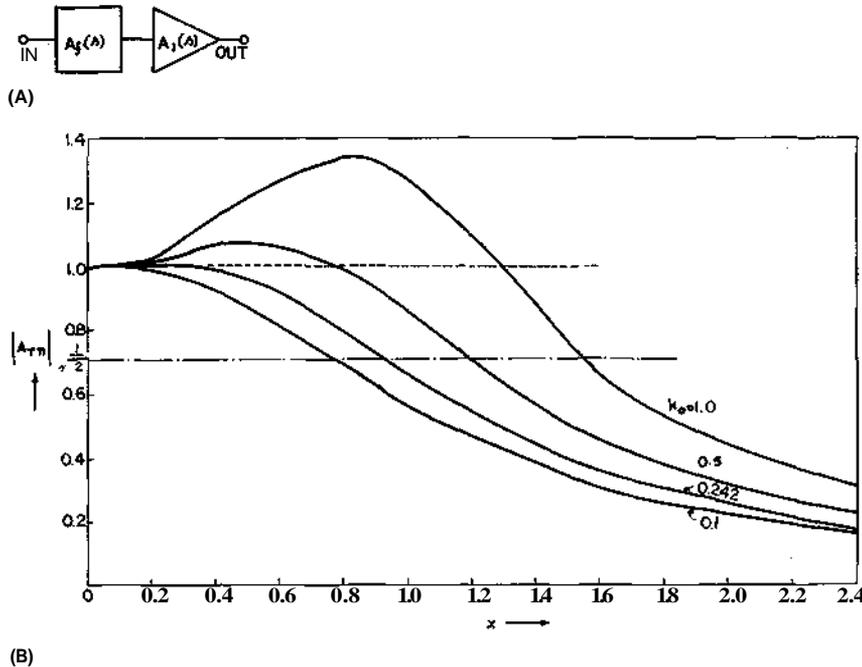


Fig. 2. (A) High-frequency compensation using the AFB. (B) Frequency response curves of the compensated amplifier.

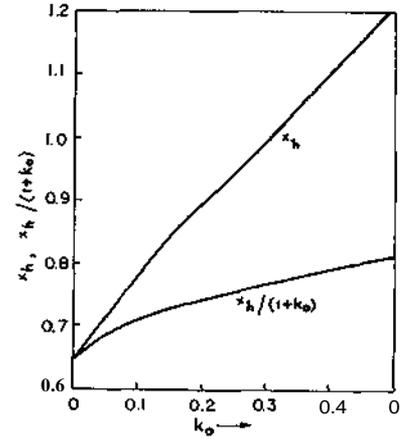


Fig. 3. Bandwidth and GBW of the compensated amplifier as a function of k_o .

From the frequency response curves shown in Fig. 2(B), it can be seen that for $k_o > 0.242$ we get peaking, and that if a small amount of peaking can be tolerated, then we can make $x_h > 1$, i.e., the AFB also boosts the bandwidth. The condition for $x_h > 1$ is $IA_{-}(jx)^2, I > 1/2$, which, combined with (7), gives $k_o > 0.321$. The

variation of xh with k_o is shown in Fig. 3. With $k_o > 0.242$, there occurs a further improvement in the GBW, as shown by the increase of $xh/(1+k_o)$ with k_o (Fig. 3). Further theoretical work on modifications of the scheme for more effective bandwidth and GBW boosting is in progress. Experimental work is also being

planned for verifying the theoretical findings. These results will be published in due course.

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